

PATENT Customer Number 22,852 Attorney Docket No. 04329.2270-01000

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:) Notice of Allowance Dated: March 29, 2004) Confirmation No.: 4805
Shinsuke Sakamoto et al.	
Application No.: 10/081,109) Group Art Unit: 2815
Filed: February 25, 2002) Examiner: Lee Eugene
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND WIRING ARRANGING METHOD THEREOF	
Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450	

Sir:

Dated: June 28, 2004

SUBMISSION OF REPLACEMENT DRAWING

Filed herewith are 2 sheets of replacement patent drawing containing Figures

1-4. It is requested that this drawings be made of official record in the above-identified patent application.

If there are any fees due in connection with the filing of this drawing, please charge the fees to our Deposit Account No. 06-0916. If a fee is required for an extension of time under 37 C.F.R. 1.136 not accounted for above, such an extension is requested and the fee should also be charged to our Deposit Account.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

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